

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	0	(liquid crystal display data lines gate pixel electrodes first signal wire ground shift clock second phase difference resistor connection timing controller parallel).clm.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	AND	OFF	2008/03/11 10:48

3/11/08 10:48:38 AM